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EHT
June 2, 2008

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Re: **CERTIFICATE OF CORRECTION**
U. S. Letters Patent No. 7,340,495
Issued: March 4, 2008
For: **SUPERIOR MISALIGNED MEMORY LOAD
AND COPY USING MERGE HARDWARE**
Inventor: Buxton et al.
Our File No. 42390.P15761

Dear Sir:

Enclosed is the Certificate of Correction for the above-referenced patent.
This request for correction is made under rule 322 of the Rules of Practice and 35 U.S.C. Section 254.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Edwin H. Taylor
Edwin H. Taylor
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EHT/dn
Enclosures

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,340,495
DATED : March 4, 2008
INVENTOR(S) : Buxton et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 4, at line 63, delete "modem" and insert --modern--.

MAILING ADDRESS OF SENDER
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PATENT NO. 7,340,495

Certificate of Correction (PTO Form 1050)-Amended